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| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|--|----------------|-------------------------|---------------------|------------------|
| 09/980,430 | 03/29/2002 | Aart Zeger van Halteren | 47161-00031USPX | 3407 |
| 30223 75 | 590 10/17/2006 | EXAMINER | | |
| JENKENS & GILCHRIST, P.C. 225 WEST WASHINGTON | | | LE, HUYEN D | |
| SUITE 2600 | | | ART UNIT | PAPER NUMBER |
| CHICAGO, IL 60606 | | | 2615 | |

DATE MAILED: 10/17/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Application/Control Number: 09/980,430

Art Unit: 2615

DETAILED ACTION

1. The reply brief filed 07/24/2006 has been entered and considered. The application has been forwarded to the Board of Patent Appeals and Interferences for decision on the appeal.

Responding to the arguments about "the Electric Circuit Board" in the Sone reference, the Applicant should note that claims 8 and 31 do not claim a specific construction and/or functions of an electric circuit board that are different from the circuit board of Sone, as broadly claimed, Sone does teach an electric circuit board that comprises a board (4, 40, 42, 44, 48) with electrical terminals or electrically conductive patterns (50, 52, 22, 24) on its surfaces (also see col. 4, lines 31-34 and lines 36-40 and col. 6, lines 65-67).

Responding to the arguments about claims 29 and 31, the Applicant should note that Sone does teach the electric circuit board that includes the electronics such as the terminal patterns as a means for electrical connections to an external device, mounting circuits, or elements of the electro-acoustic transducer (col. 4, lines 30-41) for processing the signals in the device. Futher, Lee teaches the board (3b, figures 1, 3 and see the text for these figures) that includes signal processing electronics since the outer terminals (33b) of the board are connected to a printed circuit board of a phone (col. 3, lines 65-68), and the board (3b) is used for generating calling sounds and also used as a circuit board for the coil.

Responding to the arguments about claims 27 and 35, as broadly claimed, Sone teaches a surface portion of the electric circuit board is positioned against the coil or the lead portions of the coil by adhesion (col. 4, lines 57-61 and col. 6, lines 61-68 through col. 7, lines 1-5).

Responding to the arguments about that "Lee's vibration member 3b is not an electric circuit board", the examiner refers to the "Grounds of Rejection" in the Revised Examiner's

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Answer or the Final Office Action mailed May 19, 2004. Further, Lee does disclose that the second vibration member (3b) is used as a circuit board for the coil (col. 3, lines 50-52). This vibration member (3b) is made of a synthetic resin (col. 3, lines 40-41) and has the electrical terminals (13b, 33b, and 43b) on its surface.

Conclusion

2. Any inquiry concerning this communication or earlier communications from the examiner should be directed to HUYEN D. LE whose telephone number is (571) 272-7502. The examiner can normally be reached on 9:30AM-6:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, SINH TRAN can be reached on (571) 272-7564. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

HL

October 14, 2006

PRIMARY EXAMINER